

09/06/00

ISSUE CLASSIFICATION	
Class	Subclass

Best Available Copy

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

APPLICATION NO.
09/656541

CONT/PRIOR

CLASS
703

SUBCLASS
71

ART UNIT
2123

EXAMINER

CHAPPA

APPLICANTS

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TITLE

High speed software driven emulator comprised of a plurality of emulation processors with a method to allow high speed bulk read/write operation synchronous DRAM while refreshing the memory.

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

**TERMINAL
DISCLAIMER**

DRAWINGS

CLAIMS ALLOWED

Sheets Drwg.

Figs. Drwg.

Print Flg.

Total Claims

Print Claim for O.G.

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☒ The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. 6,618,698

☐ The terminal ____ months of this patent have been disclaimed.

(Assistant Examiner)

(Date)

(Primary Examiner)

(Date _____)

(Legal Instruments Examiner)

(Date _____)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

ISSUE BATCH NUMBER

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Form **PTO-436A**
(Rev. 6/99)

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(Attached in pocket on right inside flap)

(FACE)